



FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

> INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))

Attor	ney	D	ock	et	No.
GR	00	Р	15	79)

Applic. No.

09/815,657

Applicant

Jenoe Tihanyi

Filing Date

Group Art Unit

March 23, 2001

2822

U.S. PATENT DOCUMENTS

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EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
MP	Α	5,844,273	12/01/98	Konishi			
	В						
	С						
	D						
	E						
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FOREIGN PATENT DOCUMENT

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRAI YES	NSL. I NO
J							•
К							
L							
М							
N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

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P

B. Jayant Baliga et al.: "The Accumulation-Mode Field-Effect Transistor: A New Ultralow On-Resistance MOSFET", IEEE Electron Device Letters, Vol. 13, No.

8, August 1992, pp. 427, 428

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if pot in conformance and not considered. Include copy of this form with next communication to applicant.